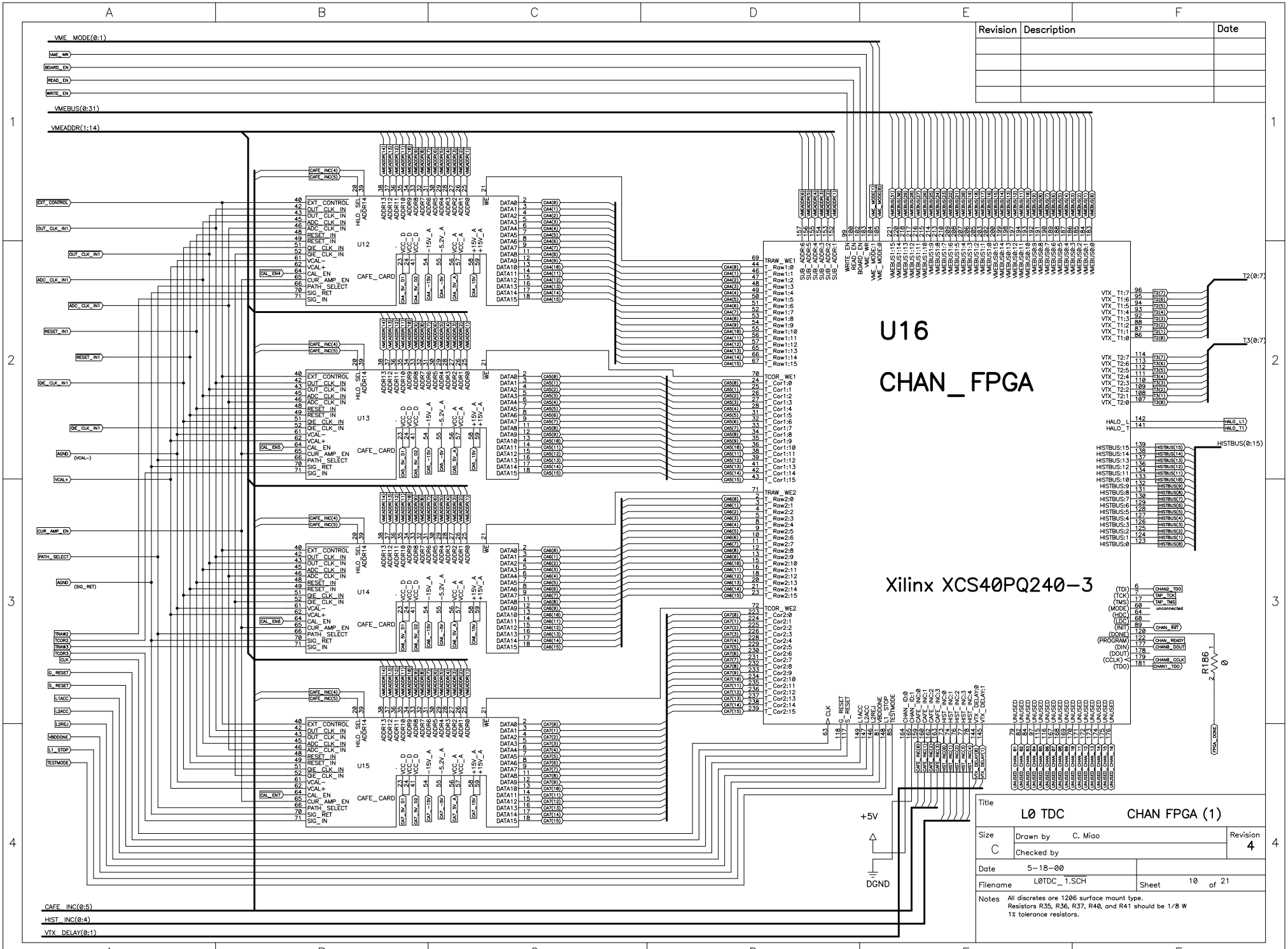
U16
CHAN_FPGA
Xilinx XCS40PQ240-3
U12
U13
U14
U15
CAFE_CARD
VME_MODE(0:1)
VMEBUS(0:31)
VMEADDR(1:14)
CAFE_INC(0:5)
HIST_INC(0:4)
VTX_DELAY(0:1)
HISTBUS(0:15)
T2(0:7)
T3(0:7)
+5V
DGND
R186
0
Revision
Description
Date
Title
L0 TDC
CHAN FPGA (1)
Size
C
Drawn by
C. Miao
Checked by
Revision
4
Date
5-18-00
Filename
L0TDC_1.SCH
Sheet
10
of
21
Notes
All discretes are 1206 surface mount type.
Resistors R35, R36, R37, R40, and R41 should be 1/8 W
1% tolerance resistors.