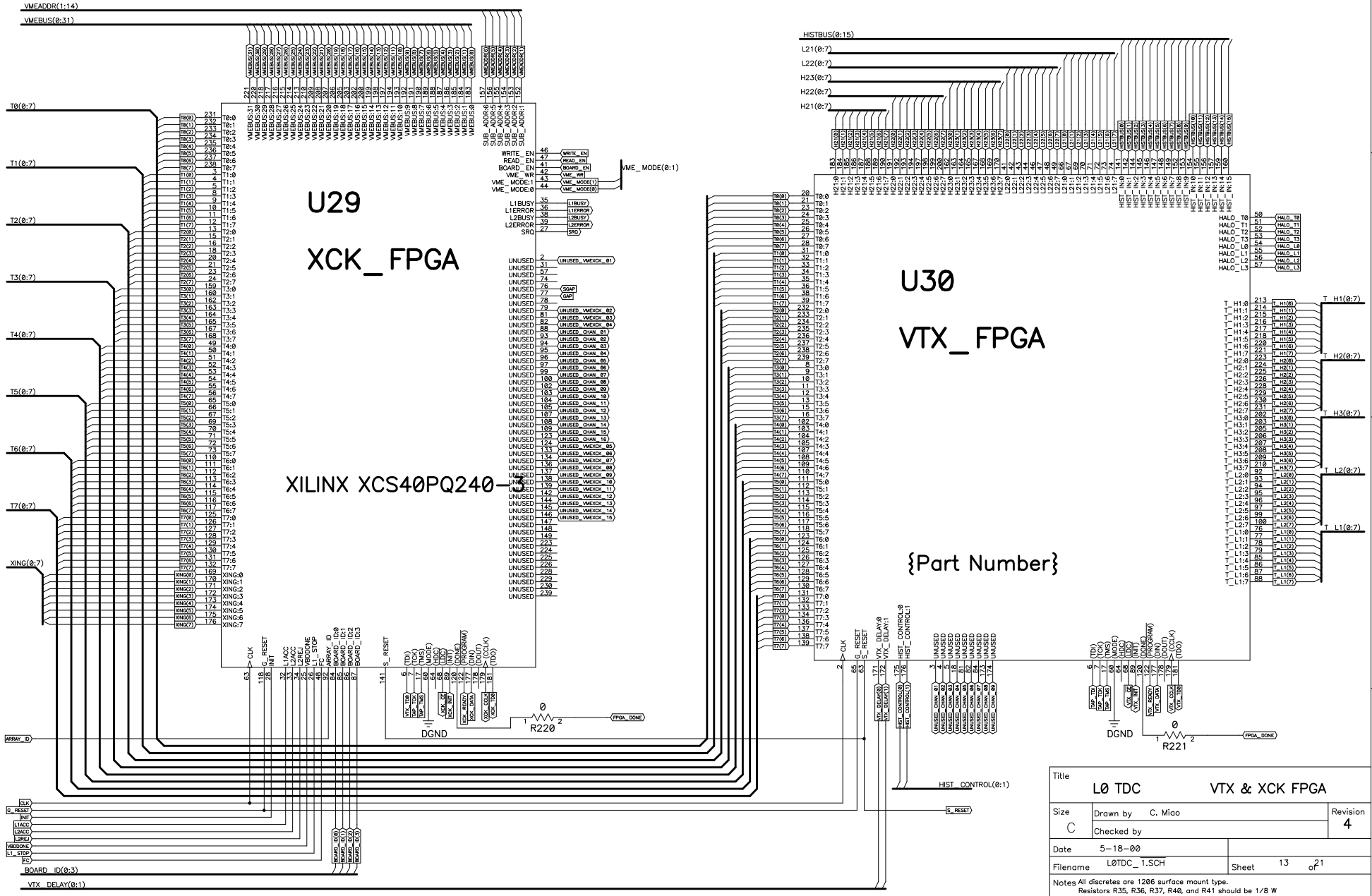
Revision	Description	Date



Title: L0 TDC — VTX & XCK FPGA

Size: C

Drawn by: C. Miao

Checked by:

Revision: 4

Date: 5-18-00

Filename: L0TDC_1.SCH

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Notes: All discretes are 1206 surface mount type.
Resistors R35, R36, R37, R40, and R41 should be 1/8 W
1% tolerance resistors.