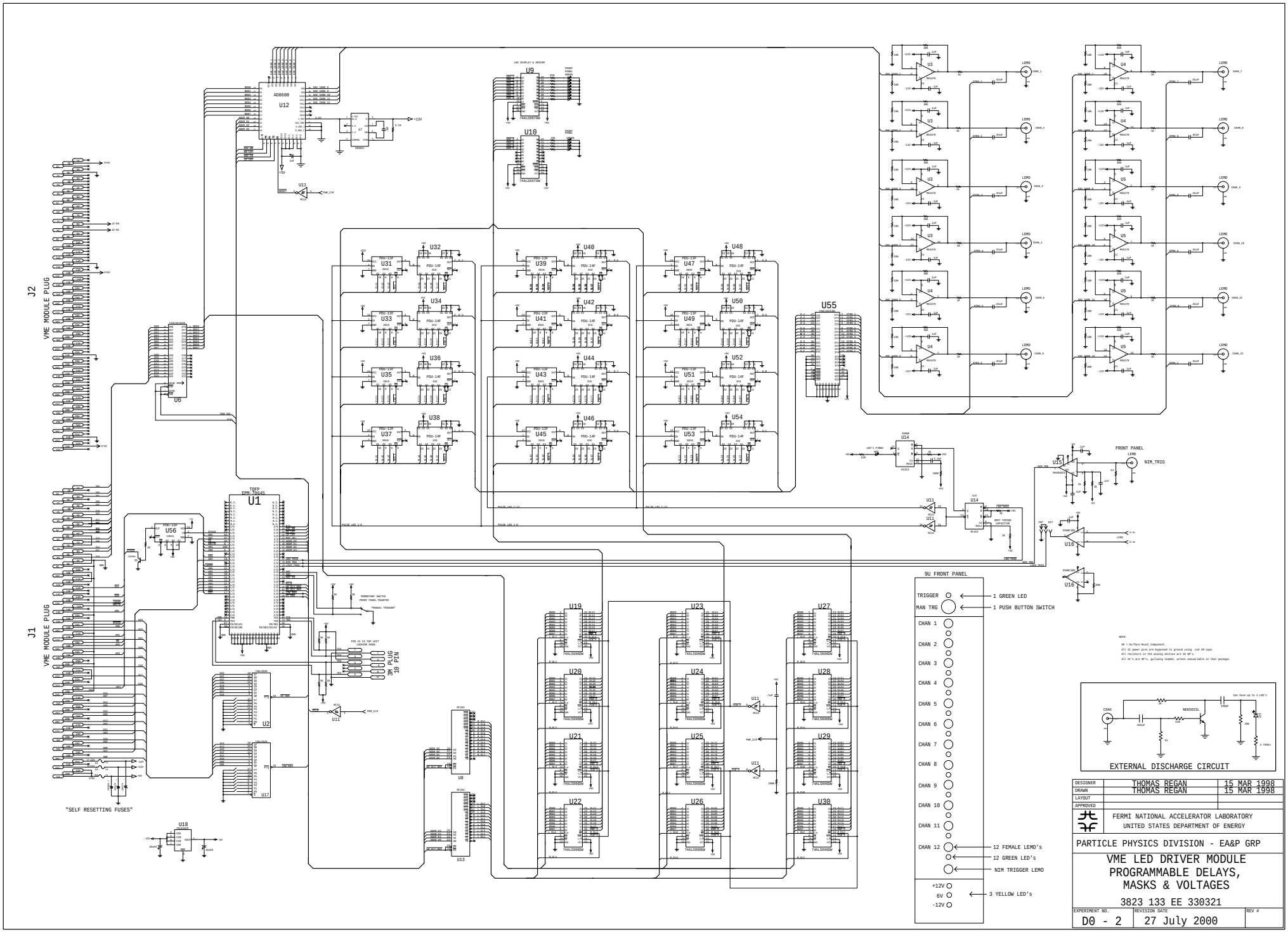
J2
VME MODULE PLUG
J1
VME MODULE PLUG
"SELF RESETTING FUSES"
U12
AD8600
U9
U10
74ALS867DW
U11
U7
U6
U1
EPM 7064S
U2
U17
U8
U13
U18
U55
U14
U15
U16
U56
U19
U20
U21
U22
U23
U24
U25
U26
U27
U28
U29
U30
74ALS996DW
U32
U33
U34
U35
U36
U37
U38
U39
U40
U41
U42
U43
U44
U45
U46
U47
U48
U49
U50
U51
U52
U53
U54
PDU-14F
PDU-13F
U3
U4
U5
MAX270
LEMO
FRONT PANEL
NIM_TRIG
MOMENTARY SWITCH
FRONT PANEL MOUNTED
"MANUAL TRIGGER"
PIN #1 IS TOP LEFT
LOOKING DOWN.
3M PLUG
10 PIN
9U FRONT PANEL
TRIGGER
MAN TRG
CHAN 1
CHAN 2
CHAN 3
CHAN 4
CHAN 5
CHAN 6
CHAN 7
CHAN 8
CHAN 9
CHAN 10
CHAN 11
CHAN 12
+12V
6V
-12V
1 GREEN LED
1 PUSH BUTTON SWITCH
12 FEMALE LEMO's
12 GREEN LED's
NIM TRIGGER LEMO
3 YELLOW LED's
NOTE:
SM = Surface Mount Component.
All IC power pins are bypassed to ground using .1uF SM caps.
All resistors in the analog section are 5% SM's.
All IC's are SM's, gullwing leaded, unless unavailable in that package.
COAX
NE85633L
EXTERNAL DISCHARGE CIRCUIT
DESIGNER THOMAS REGAN 15 MAR 1998
DRAWN THOMAS REGAN 15 MAR 1998
LAYOUT
APPROVED
FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY
PARTICLE PHYSICS DIVISION - EA&P GRP
VME LED DRIVER MODULE
PROGRAMMABLE DELAYS,
MASKS & VOLTAGES
3823 133 EE 330321
EXPERIMENT NO. D0 - 2
REVISION DATE 27 July 2000
REV #