



D0 Status Report

Marc Buehler
UVA

All Experimenters' Meeting
31 March 2008



Data Taking

	Delivered Lum (pb ⁻¹)	Recorded Lum (pb ⁻¹)	Efficiency (%)	Comment
24 Mar	7.54	7.08	94	
25 Mar	8.38	7.73	92	27 min down for L2CAL
26 Mar	8.00	7.34	92	Multiple SMT HV trips
27 Mar	8.50	7.87	93	11 min down for clock glitch
28 Mar	4.24	3.26	77	<i>Access.</i> Started late due to problems with L1Muon and MDTs. Ran w/o L1Muon triggers and masked off 2 MDTs.
29 Mar	7.85	7.32	93	<i>Access.</i> Fixed MDTs and turned L1Muon back on. Had to disable inputs from octant 3 south fw muon into L1Muon
30 Mar	3.27	3.12	95	<i>Access.</i> Replaced L1 transmitter from MDT into L1Muon.
24-30 Mar	47.8	43.7	92	Record weekly delivered and recorded luminosity.



Significant Events

- Master Clock glitch:
 - Recent history:
 - No glitches for the past years
 - Two recent glitches: March 14 & March 27
 - Both glitches were 'Sync Timing' errors where the clock did not see the once-per-turn sync signal.
 - Expert proposes to investigate, which will require intervention during store.



Significant Events

- Friday, March 28:
 - Problem with L1Muon PS and two MDT crates on north side of Forward Muon system
 - Reconfigured DAQ to run without L1Muon readout crates, and masked off the dead MDT channels
 - Lost ~1hr
- Saturday, March 29:
 - Controlled Access (4 hrs) to fix MDTs and L1Muon
 - MDTs: Replaced PS and master-module board
 - L1Muon: Investigated PS trip problem, and were able to power up again.
 - New problem with south octant 3 A-layer MDT inputs to L1Muon trigger:
 - Masked off bad octant



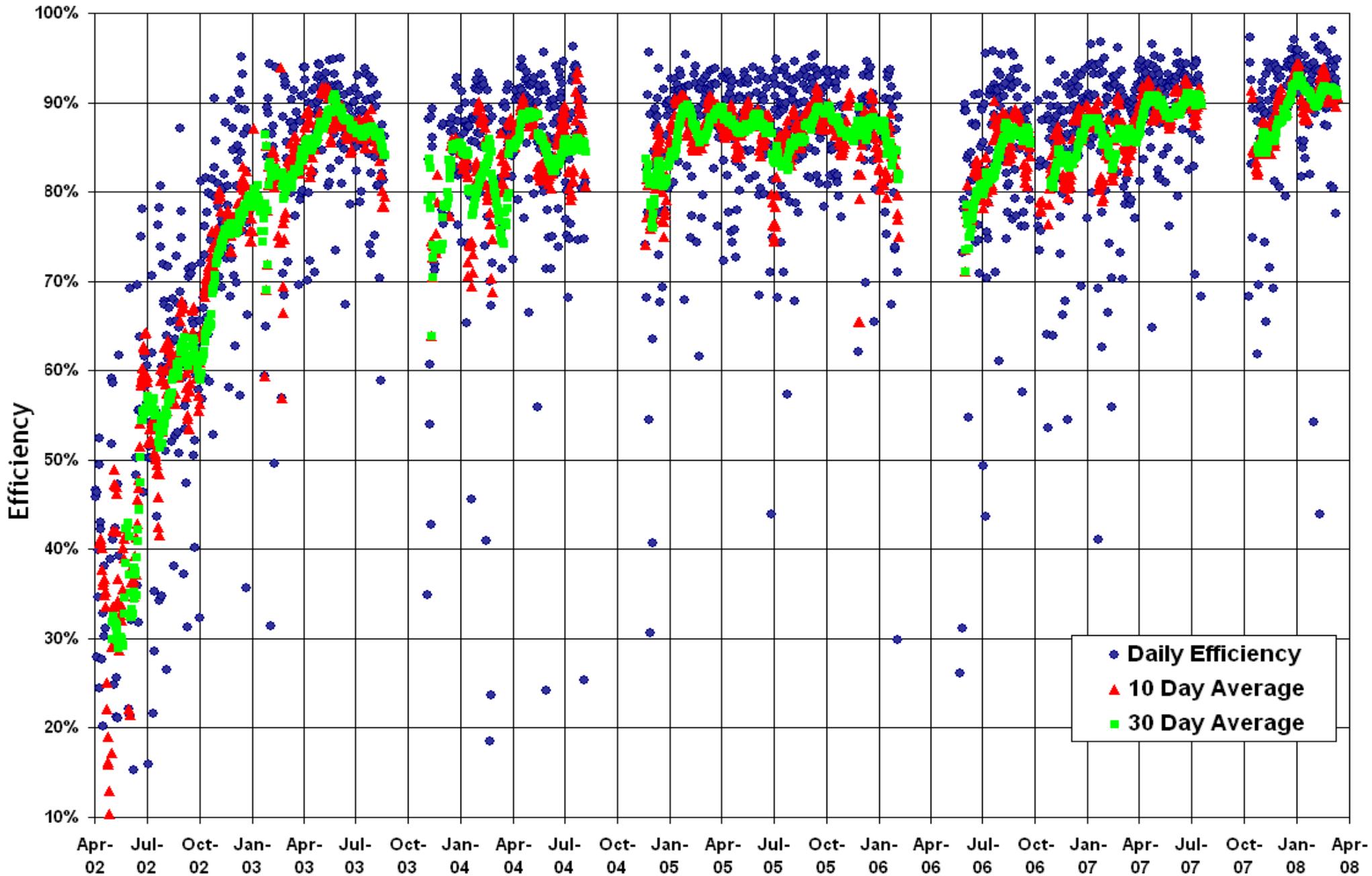
Significant Events

- Sunday, March 30:
 - Controlled Access to fix bad octant in south forward L1Muon system:
 - Replaced bad transmitter between MDT and L1Muon
- **Many thanks to AD for accommodating our CA requests, and our apologies for dropping the interlocks!**



Daily Data Taking Efficiency

19 April 2002 - 30 March 2008



31 March 2008

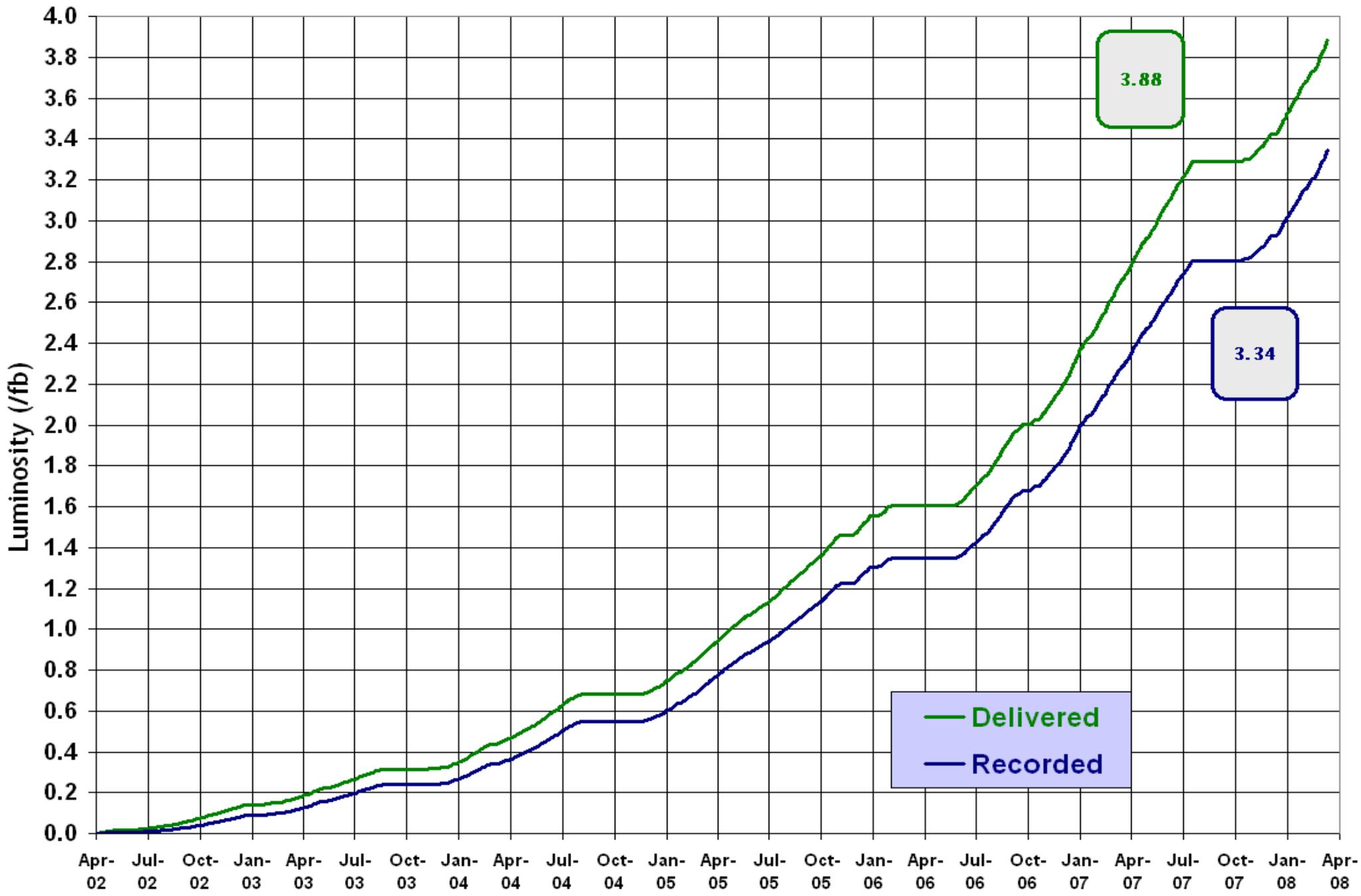
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Run II Integrated Luminosity

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