D0 Status Report
8/28/2006

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Fermilab

Data Taking for 8/21 – 8/27

Day	Delivered (pb^{-1})	Recorded (pb^{-1})	Eff. (%)	Comments
8/21 (Mon)	4.86	4.23	87	Highest recorded luminosity in a day
8/22 (Tue)	4.52	4.15	92	2nd highest recorded luminosity 3rd highest init lumi 164E30 for store 4917
8/23 (Wed)	2.66	2.25	85	A SMT low voltage supply died, taking down 1/8 of SMT. 2nd highest init lumi 168E30 for store 4919
8/24 (Thu)	2.31	1.92	84	10 hour controlled access to repair the SMT LV supply.
8/25 (Fri)	3.86	3.38	88	Lost a HV module for Luminosity counters for one hour at the beginning of store 4925. One hour downtime due to Muon readout problem.
8/26 (Sat)	4.38	3.97	91	4th highest init lumi 160E30 for store 4927
8/27 (Sun)	4.65	3.89	84	Highest init lumi 172E30 for store 4928 40 min downtime due to L1Cal problem.

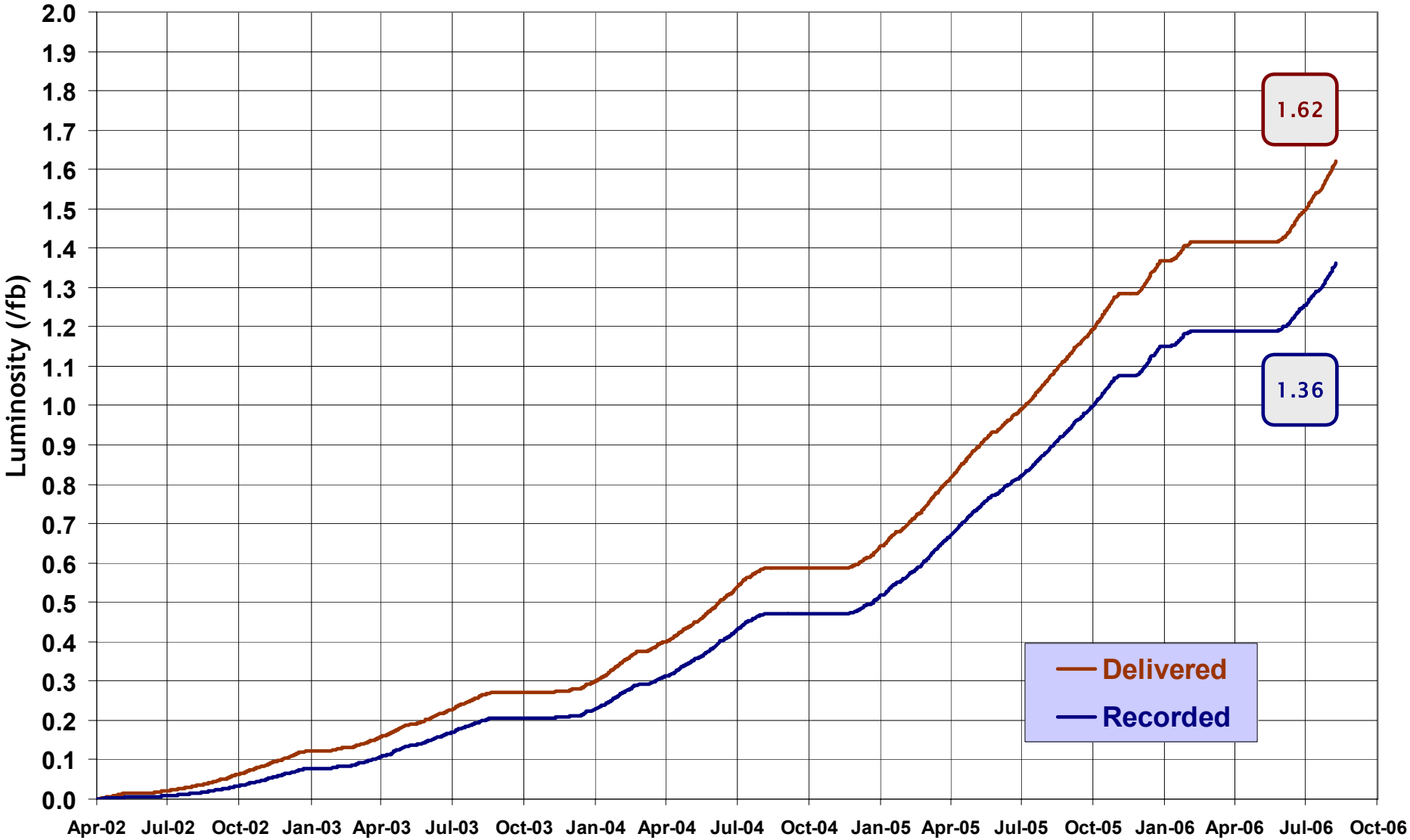
8/21-8/27	27.24	23.79	87	Highest recorded luminosity in a week

Notable Events

- 10 hour controlled access on 8/24
  - During the pbar injection for store 4919, an Interface Board LV supply tripped and could not be reset.
  - The repair required opening up one side of the detector (3 hours to open and 3 hours to close).
  - The supply is in the same location as the one failed three weeks ago.
    - But the problem seems to be different.
    - The last failure seems to be a deteriorated connector.
    - This time, a chip on the motherboard failed.
- SMT firmware upgrade
  - Last 11 ticks of SVX2 readout were compromised since the beginning of Run IIb.
    - Due to complexity in reading out both SVX2 and SVX4 chips
  - Problem was identified and solved quickly.

Run II Integrated Luminosity

19 April 2002 - 27 August 2006



Daily Data Taking Efficiency

19 April 2002 - 27 August 2006