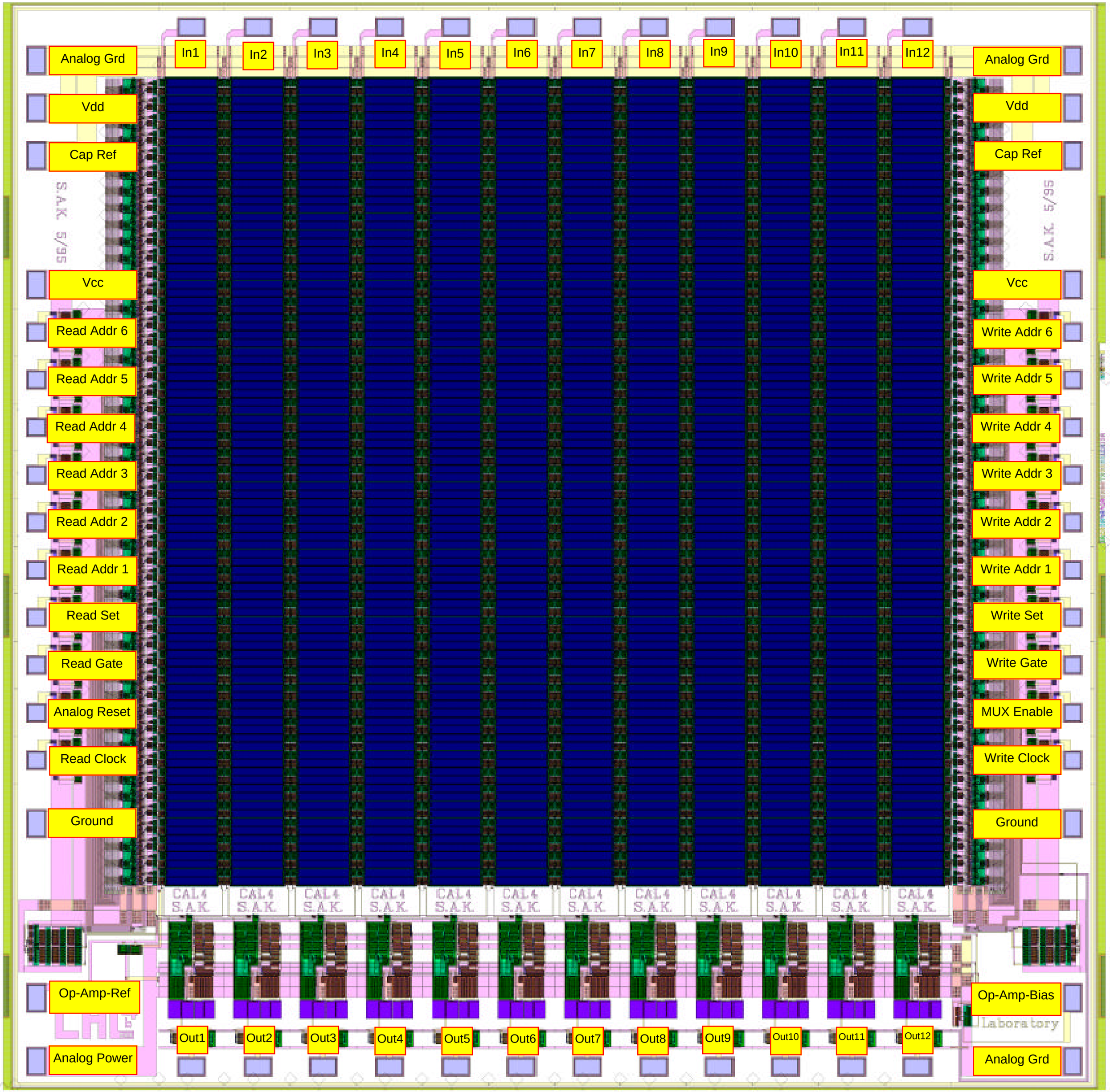
Analog Grd
Vdd
Cap Ref
Vcc
Read Addr 6
Read Addr 5
Read Addr 4
Read Addr 3
Read Addr 2
Read Addr 1
Read Set
Read Gate
Analog Reset
Read Clock
Ground
Op-Amp-Ref
Analog Power
In1
In2
In3
In4
In5
In6
In7
In8
In9
In10
In11
In12
Out1
Out2
Out3
Out4
Out5
Out6
Out7
Out8
Out9
Out10
Out11
Out12
Analog Grd
Vdd
Cap Ref
Vcc
Write Addr 6
Write Addr 5
Write Addr 4
Write Addr 3
Write Addr 2
Write Addr 1
Write Set
Write Gate
MUX Enable
Write Clock
Ground
Op-Amp-Bias
Analog Grd